

IN THE CLAIMS

Please amend claims 2, 3, 4 and 9, as follows:

1           2. (Amended) The [light] liquid crystal display device as set forth in claim 1, wherein said  
2 memory comprises:

3           first, second and third memory blocks corresponding to red, green, and blue data of said  
4 digital video signal, each of said memory blocks having at least three line memories, wherein each  
5 of said line memories stores said corresponding red, green and blue data of said digital video signal  
6 from said ADC and corresponding to one horizontal line; and

7           first, second and third multiplexers for selectively outputting data from each of said line  
8 memories of corresponding ones of said memory blocks in response to a data selection signal from  
9 said memory controller.

1           3. (Amended) The [light] liquid crystal display device as set forth in claim 2, wherein said  
2 memory controller comprises:

3           a flag generator for generating a plurality of write flag signals and a plurality of read flag  
4 signals;

5           a memory selector for generating said first and second memory selection signals for selecting  
6 said line memories in response to said write and read flag signals to block simultaneous read and  
7 write operations of a same one of said line memories; and

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8 a memory operation control circuit for controlling write and read access to said line [mories]  
9 memories in each of said memory blocks in response to said horizontal synchronization signal, said  
10 horizontal output signal, said first memory selection signal and said first and second pixel clock  
11 signals.

4. (Amended) The [light] liquid crystal display device as set forth in claim 1, wherein said  
memory, said horizontal output generator and said memory controller are constituted by a single  
chip.

9. The display apparatus of claim 6, further comprising a converter for converting said  
sampled video signal into a data signal corresponding to the number of said horizontal lines in  
accordance with a predetermined ratio determined by said difference between the pixel number and  
the reference pixel number, said data signal being provided to said displaying means.

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